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Mfp Ipa Go - Company Canon MFP Security Chip ("main board" in Figure 3), is referred to as host system. The following is a component diagram of the Canon MFP Security Chip, consisting of two dies. The FlashROM sits on one die, with all other components on another die. All of these elements are enclosed in a single

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Mfp Ipa Go The FlashROM sits on one die, with all other components on another die. All of these elements are enclosed in a single package, making up the cryptographic chip. FlashROM Register CPU Work memory Disk I/O Cryptographic ... Canon MFP Security Chip ISO/IEC19790 Security Policy Functional analysis of DEG between PAR and Page 7/28

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The only limit from the MFP itself is the capability of the MFP to display a user interface to the workings of the application. As many of the applications are based around custom printing, scanning and authentication requirements, the MFP manufacturers that use this method gravitate towards these core technologies in the user interface.

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